

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(power near5 consump\$5) same (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	USPAT	OR	OFF	2006/03/27 10:07
L2	1	(power near5 consump\$5) same (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:07
L3	1	(power near5 consum\$7) same (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:19
L4	0	(power near5 (usage using)) same (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:10
L5	1	(power near5 (usage using)) and (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:10
L6	0	(power near5 (usage using)) and (odd near impedance) and (time period) near5 (twice two) near3 (delay near transmi\$7)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 10:10
L7	2	(differen\$7 near voltage) same (first one primary) near buffer\$5 near5 source and (second other) near buffer\$5 same source and (first one primary) near transistor and (second other two) near transistor same buffer\$5	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:29
L8	1	(differen\$7 near voltage) same (first one primary) near buffer\$5 near5 source and (second other) near buffer\$5 same source and (first one primary) near transistor and (second other two) near transistor same buffer\$5 and (third three) near resistor and (four fourth) near resistor	US-PGPUB; USPAT	OR	OFF	2006/03/27 10:29
L9	0	(differen\$7 near voltage) same (first one primary) near buffer\$5 near5 source and (second other) near buffer\$5 same source and (first one primary) near transistor and (second other two) near transistor same buffer\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 10:29

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L10	0	(differen\$7 near voltage) same (first one primary) near buffer\$5 near5 source and (second other) near buffer\$5 same source and (first one primary) near transistor and (second other two) near transistor same buffer\$5 and (third three) near resistor and (four fourth) near resistor	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 11:23
L11	12	(VENCA near ALESSANDRO).inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 11:24
L12	68	(ALINI near ROBERTO).inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 11:25
L13	15	(POSAT near BARIS).inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 11:25